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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10064356	FILING DATE 07/04/2002	CLASS 438	SUBCLASS 1	GAU 2843	EXAMINER 2825 Siek
<b>** APPLICANTS:</b> Yang Chuen; Wang Chien-Ming; Lai Chien-Wen; Tsay Cheng-Shyan;					
<b>** CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b>					
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<b>PG-PUB</b> <input type="checkbox"/> <b>DO NOT PUBLISH</b> <input type="checkbox"/> <b>RESCIND</b> <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no				NAUP0473USA	
Verified and Acknowledged Examiners's initials					
TITLE : Method of dividing a semiconductor integrated circuit pattern					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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